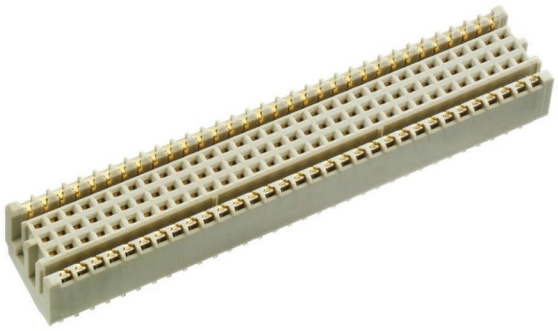
Product Data Sheet

DIN 41612 VME 64x Female,
Part No. 306-61067-12



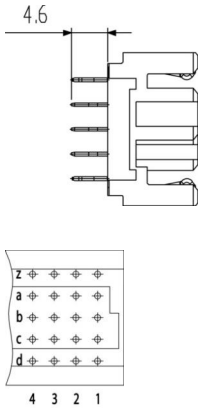


Illustration similar

Perpendicular | Press-fit | Rugged

- tail length 4.6 mm
- number of positions: 160
- press-fit technology
- performance level 1
- without a flange

» to product on www.ept.de

» to product group DIN 41612

ept GmbH | Bergwerkstr. 50 | 86971 Peiting | Tel. +49 (0) 8861 / 25 01 0 | Fax +49 (0) 8861 / 55 07 | sales@ept.de | **www.ept.de**

Product Data Sheet

DIN 41612 VME 64x Female, Part No. 306-61067-12

Technical Specifications

Basics

Specification	IEC 61076-4-113
Performance Level	1
No. of Contacts	160
Termination Technology	press-fit technology
Termination Length	4.6 mm
Operating Temperature Range	-55°C to +125°C

Material

Insulator Material	glass-fiber reinforced PBT, UL 94 V-0
CTI value *IEC 60112*	200
Contact Material	copper alloy

Mechanical

Pitch	2.54 mm
Mating Force	160 N
Separating Force per Pin	> 0.15 N
Durability	500 mating cycles

Electrical

Operational Current	1.5 A
Test Voltage	1000 V
Contact Resistance	20 mΩ
Clearance and Creepage	abc ≥ 1.2 mm, zd ≥ 1.0 mm
Insulation Resistance	10^4 MΩ

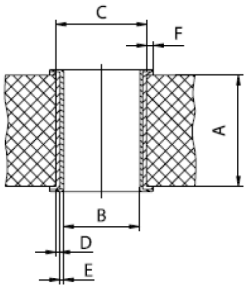
Approval / Compliance

UL file	E130314
Environment	RoHS compliant

ept GmbH | Bergwerkstr. 50 | 86971 Peiting | Tel. +49 (0) 8861 / 25 01 0 | Fax +49 (0) 8861 / 55 07 | sales@ept.de | **www.ept.de**

Hole Specifications

Plated through-hole according to IEC 60352-5



Material	chem. Sn Leiterplatten
Nominal Hole	**Ø 1.0 mm**
A PCB Thickness	min. 1.44 mm
B Plated Hole	Ø 1.0 +0.09 / -0.06 mm
C Drill Hole	1.15 ±0.025 mm
D Cu Plating	min. 25 µm
E Surface	chem. Sn Schicht, max. 1.5 µm
F Annular Ring	min. 0.1 mm

Material	Ni, Au Leiterplatten
Nominal Hole	**Ø 1.0 mm**
A PCB Thickness	min. 1.44 mm
B Plated Hole	Ø 1.0 +0.09 / -0.06 mm
C Drill Hole	1.15 ±0.025 mm
D Cu Plating	min. 25 µm
E Surface	Ni, Au Schicht, 0.05 - 0.2 µm Au über 2.5 - 5 µm Ni
F Annular Ring	min. 0.1 mm

Material	rein Cu Leiterplatten
Nominal Hole	**Ø 1.0 mm**
A PCB Thickness	min. 1.44 mm
B Plated Hole	Ø 1.0 +0.09 / -0.06 mm
C Drill Hole	1.15 ±0.025 mm
D Cu Plating	min. 25 µm
E Surface	OSP, z.B. GLICOAT-SMD (F2) mit 0.12 - 0.15 µm
F Annular Ring	min. 0.1 mm

Material	HAL Sn Leiterplatten
Nominal Hole	**Ø 1.0 mm**
A PCB Thickness	min. 1.44 mm
B Plated Hole	Ø 1.0 +0.09 / -0.06 mm
C Drill Hole	1.15 ±0.025 mm
D Cu Plating	min. 25 µm
E Surface	HAL Sn, 5 - 15 µm
F Annular Ring	min. 0.1 mm

ept GmbH | Bergwerkstr. 50 | 86971 Peiting | Tel. +49 (0) 8861 / 25 01 0 | Fax +49 (0) 8861 / 55 07 | sales@ept.de | **www.ept.de**

Drawings

Component data for this product you can request through sales@ept.de