

hm2.0 Male, type AB19, Part No. 243-63320-15

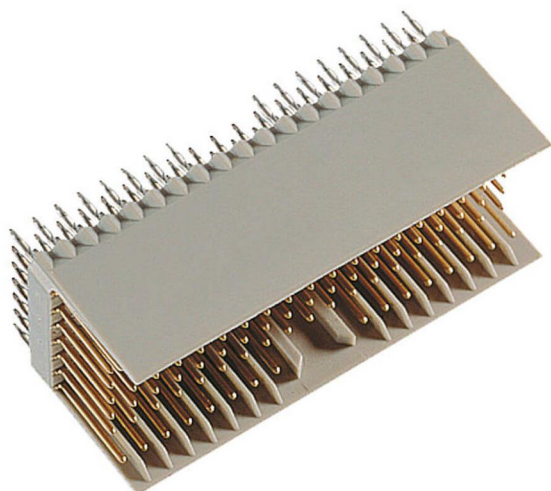


Diagram illustrating a 60x60 grid of cells, divided into two main sections (left and right) by a vertical gap between columns 10 and 11. The grid is composed of 6 rows and 20 columns. The cells are colored black or white, forming a pattern that suggests a complex arrangement, possibly related to the 60x60 grid mentioned in the text.

Illustration similar



Perpendicular



Press-fit

- number of positions: 127
- tail length 3.7 mm
- for PCB thickness > 2.2 mm
- Tested in accordance with IEC 61076-4-101



» to product on www.ept.de



» to product group hm 2.0

Product Data Sheet

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Technical Specifications

Basics

Specification	IEC 61076-4-101
Performance Level	2
No. of Contacts	127
Termination Technology	press-fit technology
Termination Length	3.7 mm
Operating Temperature Range	-55°C to +125°C

Material

Insulator Material	glass-fiber reinforced PBT, UL 94 V-0
CTI value <i>IEC 60112</i>	200
Contact Material	Bronze

Mechanical

Pitch	2.0 mm
Mating Force per Pin	Contact: max. 0.75 N, shielding: max. 1 N
Separating Force per Pin	Contact: min. 0.15 N, shielding: min. 0.15 N
Durability	> 250 mating cycles

Electrical

Operational Current	1.5 A at +20°C, 1.0 A at +70°C
Test Voltage	750 V r.m.s.
Contact Resistance	max. 20 mΩ
Clearance and Creepage	≥ 0.8 mm
Insulation Resistance	at least 10 ⁴ MΩ
Data Transfer Rate	3.125 Gbit/s

Approval / Compliance

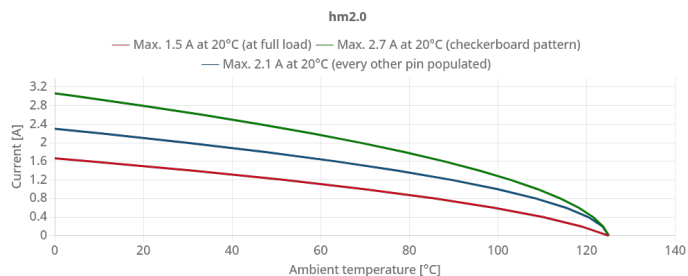
UL file	E130314
Environment	RoHS compliant

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Derating Diagram



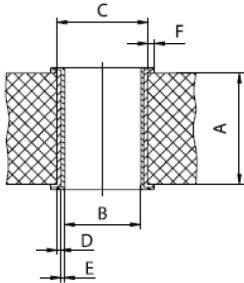
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Hole Specifications

Plated through-hole according to IEC 60352-5



Material	chem. Sn Schicht
Nominal Hole	Ø 0.6 mm
A PCB Thickness	min. 2.2 mm
B Plated Hole	Ø 0.60 ±0.05 mm
C Drill Hole	0.70 ±0.02 mm
D Cu Plating	min. 25 µm
E Surface	max. 1.5 µm; chem. Sn Leiterplatten
F Annular Ring	min. 0.1 mm

Material	Ni, Au Leiterplatten
Nominal Hole	Ø 0.6 mm
A PCB Thickness	min. 2.2 mm
B Plated Hole	Ø 0.60 ±0.05 mm
C Drill Hole	0.70 ±0.02 mm
D Cu Plating	min. 25 µm
E Surface	Ni, Au Schicht, 0.05 - 0.2 µm Au über 2.5 - 5 µm Ni
F Annular Ring	min. 0.1 mm

Material	rein Cu Leiterplatten
Nominal Hole	Ø 0.6 mm
A PCB Thickness	min. 2.2 mm
B Plated Hole	Ø 0.60 ±0.05 mm
C Drill Hole	0.70 ±0.02 mm
D Cu Plating	min. 25 µm
E Surface	OSP*, z.B. GLICOAT-SMD (F2) mit 0.12 - 0.15 µm
F Annular Ring	min. 0.1 mm

Material	HAL Sn Leiterplatten
Nominal Hole	Ø 0.6 mm
A PCB Thickness	min. 2.2 mm
B Plated Hole	Ø 0.60 ±0.05 mm
C Drill Hole	0.70 ±0.02 mm
D Cu Plating	min. 25 µm
E Surface	HAL Sn, 5 - 15 µm
F Annular Ring	min. 0.1 mm

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Modifications

Available on request

- custom loading
- Other contact plating

Drawings

Component data in 2D and 3D format you can download here:

[» PDF](#)

[» 3D IGES](#)

[» 3D STEP](#)

[» 3D PDF](#)